

In the Claims:

Please amend the claims as indicated below.

1. (currently amended) A voltage regulator for supplying a low current load with a more regulated voltage supply and for supplying a high current load with a less regulated voltage supply comprising:

high current regulation means for providing a coarse level of voltage regulation to a common supply voltage delivered to a high current load, said high current regulation means including a control means; and

low current feedback regulation means for providing a fine level of regulation to said common supply voltage delivered to a low current load, the low current feedback regulation means having an output line connected to said control means of the high current regulation means whereby an output level of the feedback regulation means influences said high current regulation means;

wherein the low current feedback regulation means comprises a bandgap regulator feeding a comparator and a low current output transistor, the low current output transistor connected to the common supply and to a voltage divider having a loop back to the comparator; and

wherein the output transistor is connected to said output line coupled to said control means of the high current regulation means.

2. (previously presented) The voltage regulator of claim 1 wherein said high current regulation means comprises a depletion NMOS transistor with source and drain electrodes connecting the common supply voltage to the high current load.

3. (cancelled)

4. (cancelled)

5. (previously presented) A voltage regulator for supplying a low current load with a more regulated voltage supply and a high current load with a less regulated voltage supply comprising:

a first input terminal connected to a common voltage supply, the input terminal connected to a bandgap reference circuit feeding a comparator with an output line communicating with a voltage divider, the voltage divider having a first connection to the low current load and a second connection as a feedback path to the comparator, the comparator driving a current sinking transistor having an electrode connected to the common voltage supply and another electrode connected to the feedback path associated with the voltage divider; and

a second input terminal connected to the common voltage supply which, in turn, is connected to an MOS transistor having a gate connected to the low current load, the MOS transistor having an electrode connected to the high current load whereby the low and high current loads are supplied current from the same common voltage supply but with different voltage regulation.

6. (original) The voltage regulator of claim 5 wherein the high current load comprises a serially connected string of capacitors associated with a charge pump.

7. (original) The voltage regulator of claim 5 wherein the low current load comprises a plurality of clock circuits associated with a charge pump.

8. (cancelled)

9. (currently amended) The voltage regulator of claim [8] 5 wherein the first low current load comprises an oscillator having a low voltage pulse train output signal.

10-12. (cancelled)

13. (currently amended) The voltage regulator of claim [8] 5 wherein the ~~second current driver~~ MOS transistor associated with the second input terminal is a depletion NMOS transistor with a ~~first electrode coupled to the common supply voltage and a second electrode connected to the second output line~~.

14. (currently amended) The voltage regulator of claim [8] 5 wherein the voltage divider comprises first and second resistors connected in series, the connection of said resistors being ~~said tap connected to the feedback path~~.

15. (original) The voltage regulator of claim 14 wherein said first and second resistors are matched.

16-20. (cancelled)